



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/691,080 **Confirmation No.:** 9280

Applicants: Barry E. Burke et al.

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TC/A.U.: 2818

Examiner: C. Luu

Docket No.: MIT8431L

For: High-Yield, Single-Level Gate Charge-Coupled Device Design and Fabrication

MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
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MAILING DATE OF NOTICE OF ALLOWANCE:
JANUARY 25, 2007

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited on the date shown below with the United States Postal Service in an envelope with sufficient postage as First Class Mail addressed to: Mail Stop Issue Fee, Commissioner For Patents P.O. Box 1450, Alexandria, VA 22313-1450

April 4, 2007
Theresa A. Lober

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed January 25, 2007, herewith are provided:

- Completed Form PTOL-85 Part B- Fee Transmittal;
- Copy of completed Form PTOL-85 Part B-Fee Transmittal; and
- Authorization to charge \$700 issue fee, advance copy fees, any deficiency in stated fees, and to credit any overpayment, to Deposit Account Number 19-2553.

Respectfully submitted,

Date: *April 4, 2007*
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